

1. Simulation

2. Formal Verification



Formal Verification Microprocessors Automatic Superscalar

David A. Cyrluk



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Formal Verification of Microprocessors Miroslav Velez, 2015-12-01 Formal verification is the mathematical proof of correctness of computer systems. Microprocessors are the most important components in computer systems and are increasingly being used in safety critical applications e.g. to monitor the health of patients, to control the engines and brakes of cars, to drive autonomous vehicles and fly autonomous aircraft, and to control weapons systems. As such, the correctness of microprocessors is a matter of public safety and national security. Furthermore, for the companies that design and manufacture microprocessors or systems controlled by them, the correctness of microprocessors is a matter of business success or failure. Statistics from industrial microprocessor designs indicate that up to 90% of the engineering effort is spent on verification, which increasingly becomes the bottleneck in developing new products. Formal verification has the potential to significantly reduce the design time while also guaranteeing complete correctness. However, previous approaches for formal verification of microprocessors either do not scale for complex designs or require prohibitive amounts of manual effort by experts. In contrast, this book presents a highly automatic and scalable method for formal verification of complex processors, including pipelined superscalar and VLIW designs.

Automated Reasoning with Analytic Tableaux and Related Methods Marta Cialdea Mayer, Fiora Pirri, 2003-09-03 This book constitutes the refereed proceedings of the International Conference on Automated Reasoning with Analytic Tableaux and Related Methods, TABLEUX 2003, held in Rome, Italy, in September 2003. The 20 revised full papers presented were carefully reviewed and selected for inclusion in the book. All current issues surrounding the mechanization of logical reasoning with tableaux and similar methods are addressed in the context of a broad variety of logic calculi.

Tools and Algorithms for the Construction and Analysis of Systems

Tiziana Margaria, Wang Yi, 2003-06-29 This book constitutes the refereed proceedings of the 7th International Conference on Tools and Algorithms for the Construction and Analysis of Systems, TACAS 2001. The 36 revised full papers presented together with an invited contribution were carefully reviewed and selected from a total of 125 submissions. The papers are organized in sections on symbolic verification, infinite state systems, deduction and abstraction, application of model checking techniques, timed and probabilistic systems, hardware design and verification, software verification, testing techniques, and tools, implementation techniques, semantics and compositional verification, logics and model checking, and ETAPS tool demonstration.

Design and Analysis of Distributed Embedded Systems Bernd Kleinjohann, K.H. (Kane) Kim, Lisa Kleinjohann, Achim Rettberg, 2013-04-17 Design and Analysis of Distributed Embedded Systems is organized similarly to the conference. Chapters 1 and 2 deal with specification methods and their analysis, while Chapter 6 concentrates on timing and performance analysis. Chapter 3 describes approaches to system verification at different levels of abstraction. Chapter 4 deals with fault tolerance and detection. Middleware and software reuse aspects are treated in Chapter 5. Chapters 7 and 8 concentrate on the distribution-related topics such as partitioning, scheduling, and communication. The book closes with a

chapter on design methods and frameworks A Systematic Methodology for Verifying Superscalar Microprocessors
Mandayam Srivas,1999 **Formal Methods in Computer-aided Design** ,2002 *Computer Aided Verification* E. Allen
Emerson,2000 Annotation This book constitutes the refereed proceedings of the 12th International Conference on Computer
Aided Verification CAV 2000 held in Chicago IL U S A in July 2000 The 35 revised full papers presented together with 9 tool
papers were carefully reviewed selected from 91 submissions The papers address all current aspects of the theory practice of
formal methods for hardware software verification Emphasis is given to verification algorithms methods tools their
implementation Proceedings of the ... ACM Great Lakes Symposium on VLSI ,2004 **Tools and Algorithms for the**
Construction and Analysis of Systems ,2004 *GLSVLSI '04* ,2004 Computer-aided Verification ,2004

Proceedings of the ASP-DAC '97, Asia and South Pacific Design Automation Conference 1997, January 28-31,
1997, Makuhari Messe, Nippon Convention Center, Chiba, Japan IEEE Circuits and Systems Society,1996 The Asia and
South Pacific conference on design automation is the second in a series of biennial international conferences It aims to
provide the CAD DA community with the opportunity to present ideas and concepts on upstream design as well as
methodologies of downstream design **31st ACM/IEEE Design Automation Conference** IEEE Circuits and Systems
Society,1994 **An Algebraic Framework for Modelling and Verifying Microprocessors Using HOL** Anthony
Fox,2001 *Formal Verification of Superscalar Microprocessors with Multicycle Functional Units, Exceptions, and Branch*
Prediction Miroslav N. Velev,Randal Bryant,2000 Abstract We extend the Burch and Dill flushing technique 6 for formal
verification of microprocessors to be applicable to designs where the functional units and memories have multicycle and
possibly arbitrary latency We also show ways to incorporate exceptions and branch prediction by exploiting the properties of
the logic of Positive Equality with Uninterpreted Functions 4 5 We study the modeling of the above features in different
versions of dual issue superscalar processors **Recent Trends in Algebraic Development Techniques** ,2001 *Proof*
Methodologies for Processor Verification David A. Cyrluk,2003 Computer & Control Abstracts ,1996 **A Framework**
for the Specification, Design, and Verification of Pipelines with Structural Hazards Mark Aagaard,1995

Simulation Symposium (SS 2001), 34th Annual IEEE,2001 Proceedings of an April 2001 symposium addressing
traditional discrete event and continuous simulation topics as well as topics in areas of distributed systems network modeling
and advances in simulation methodology and practices Material is in sections on simulation based performance analysis
network modeling and simulation VLSI circuit simulators simulation languages and environments Web based modeling and
simulation parallel and distributed simulation and advances in simulation methodology and practices Some subjects are
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